



Series 680 Program Header and Cover

FEATURES

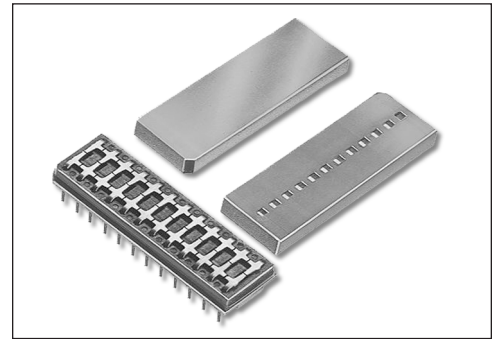
- Complete versatility for programming within the unit itself, thus eliminating the need for DIP switches in many situations.
- Available pre-programmed from Aries, or do-it-yourself using Aries Hand Tool P/N T-680. Consult Data Sheet 22002 for Programming Tool information.
- Available with plain (solid) or "top-slot" press-on covers. Plain covers protect programs while "top-slot" covers allow programming of opposing pins through square openings on top.

GENERAL SPECIFICATIONS

- STANDARD BODY AND COVER: black UL 94V-0 Glass filled 4/6 Nylon
- PINS: Brass 260 3/4-hard, ASTM B-36
- PIN PLATING: "-T" 50-150 μ [1.27 μ] min. matte Sn per ASTM B545-97(2004) e1 over 50 μ [1.27 μ] min. Ni per SAE AMS-QQ-N-290B; "-TL" 50-150 μ [1.27 - 3.81 μ] min. 60/40 Sn/Pb per SAE AMS-P-81728 over 50 μ [1.27 μ] min. Ni per SAE AMS-QQ-N-290B
- CURRENT RATING: 1 amp
- OPERATING TEMPERATURE: 221°F [105°C]

MOUNTING CONSIDERATIONS

- SUGGESTED PCB HOLE SIZE: 0.032 \pm 0.002 [0.81 \pm 0.05] dia.



CUSTOMIZATION: In addition to the standard products shown on this page, Aries specializes in custom design and production. Special materials, platings, sizes, and configurations can be furnished, depending on the quantity. **NOTE:** Aries reserves the right to change product general specifications without notice.

ORDERING INFORMATION

XX-680-19X XX X

No. of Pins — 4 thru 24
Series —
Interconnection Styles — see below
Optional Top Slot = "C" preassembled to header Style 191 only
Plating — T = Sn, TL = Sn/Pb

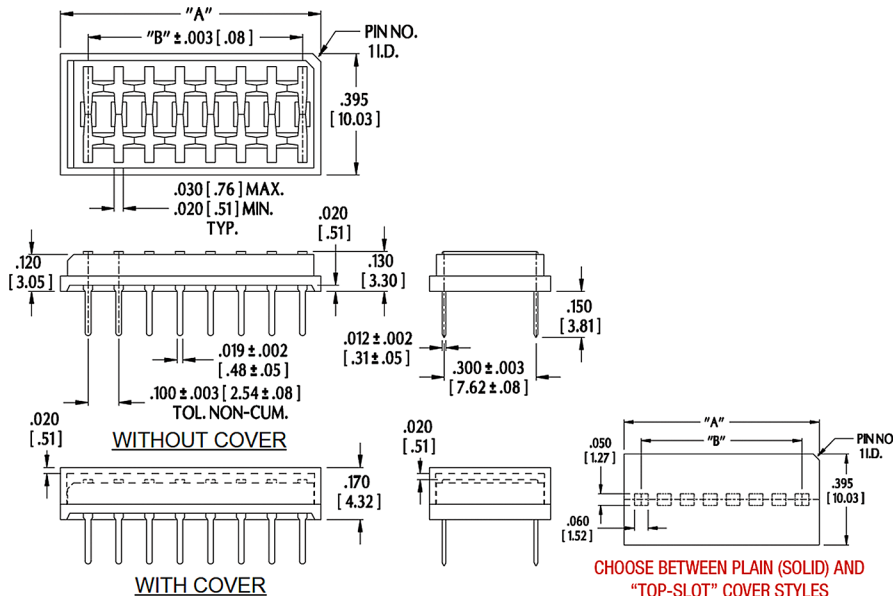
ALL DIMENSIONS: INCHES [MILLIMETERS]

ALL TOLERANCES: \pm 0.005 [0.13] UNLESS OTHERWISE SPECIFIED

"A" # PINS PER ROW \times 0.100 [2.54] +0.050 [1.27]

"B" (# PINS PER ROW -1) \times 0.100 [2.54]

CONSULT FACTORY FOR OTHER SIZES AND CONFIGURATIONS



COVER ORDERING INFORMATION

XX-68X-10

No. of Pins — 4 thru 24
Cover Series — 655 = Plain, 658 = Top Slot
Cover No. —

	STYLE 190 All pins interconnected, both adjacent and opposing
	STYLE 191 Opposing pins interconnected only.



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