

PTC thermistors for overcurrent protection

SMDs, EIA sizes 3225 and 4032, 24 V

Series/Type: B59101, B59201, B59301

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Overcurrent protection

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SMD

Applications

- Overcurrent protection
- Short circuit protection

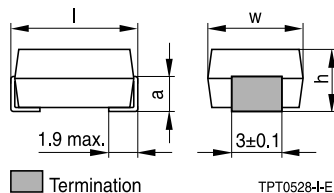
Features

- Molded epoxy encapsulation, lead-free tinned solder terminals
- Marking: Type, manufacturer's logo, reference temperature in °C
- Suitable for wave and reflow soldering
- Suitable for automatic placement
- Qualification based on AEC-Q200, Rev. D
- Moisture sensitivity level (MSL) 1 acc. to JEDEC J-STD-020D
- RoHS-compatible

Delivery mode

- Blister tape, 330-mm reel with 16-mm tape, taping to IEC 60286-3

Dimensional drawing

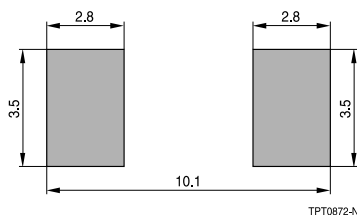


Dimensions (mm)

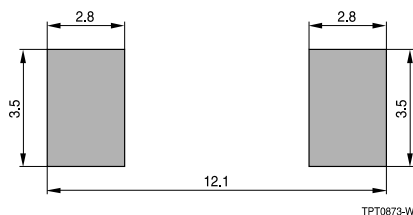
Type	h ±0.5	w ±0.5	l ±0.5	a ±0.3	Size
Reference temperature $T_{ref} = 80\text{ °C}$					
P1101	3.3	6.3	8.0	1.7	3225
P1201	3.3	6.3	8.0	1.7	3225
P1301	3.3	8.0	10.0	2.3	4032
Reference temperature $T_{ref} = 120\text{ °C}$					
P1101	3.3	6.3	8.0	1.7	3225
P1201	3.3	6.3	8.0	1.7	3225
P1301	3.3	8.0	10.0	2.3	4032

Geometry of solder pads

EIA case size 3225



EIA case size 4032



Recommended maximum dimensions (mm)

General technical data

Max. operating voltage	($T_A = 60\text{ °C}$)	V_{max}	30	V DC or V AC
Rated voltage		V_R	24	V DC or V AC
Switching cycles		N	100	
Tolerance of R_R		ΔR_R	±25	%
Operating temperature range	($V = 0$)	T_{op}	-40/+125	°C
Operating temperature range	($V = V_{max}$)	T_{op}	-40/+60	°C

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Electrical specifications and ordering codes

Type	I_R mA	I_S mA	I_{Smax} ($V = V_{max}$) A	I_r (typ.) ($V = V_{max}$) mA	R_R Ω	R_{min} Ω	Ordering code
Reference temperature $T_{ref} = 80\text{ }^\circ\text{C}$							
P1301	205	420	1.6	38	3.1	1.85	B59301P1080A062
P1201	165	340	1.0	34	4.6	2.70	B59201P1080A062
P1101	90	185	0.7	25	13	7.80	B59101P1080A062
Reference temperature $T_{ref} = 120\text{ }^\circ\text{C}$							
P1301	310	640	1.6	53	3.1	1.85	B59301P1120A062
P1201	265	545	1.0	45	4.6	2.70	B59201P1120A062
P1101	170	355	0.7	35	13	7.80	B59101P1120A062

Reliability data

Test	Standard	Test conditions	$ \Delta R_{25}/R_{25} $
Electrical endurance, cycling	IEC 60738-1	Room temperature, I_{Smax} , V_{max} Number of cycles: 100	< 25%
Electrical endurance, constant	IEC 60738-1	Storage at V_{max} and $T_{op,max}$ (@ V_{max}) Test duration: 1000 h	< 25%
Damp heat	IEC 60738-1	Temperature of air: 40 °C Relative humidity of air: 93% Duration: 56 days Test according to IEC 60068-2-78	< 10%
Rapid change of temperature	IEC 60738-1	$T_1 = T_{op,min}$ (0 V), $T_2 = T_{op,max}$ (0 V) Number of cycles: 5 Test duration: 30 min Test according to IEC 60068-2-14, test Na	< 10%
Shock	IEC 60738-1	Acceleration: 400 m/s ² Pulse duration: 6 ms; 6 × 5000 pulses	< 5%
Bending test	IEC 60738-1	Components reflow-soldered to test board Maximum bending: 2 mm Test according to IEC 60068-2-21, test Ue	< 10%

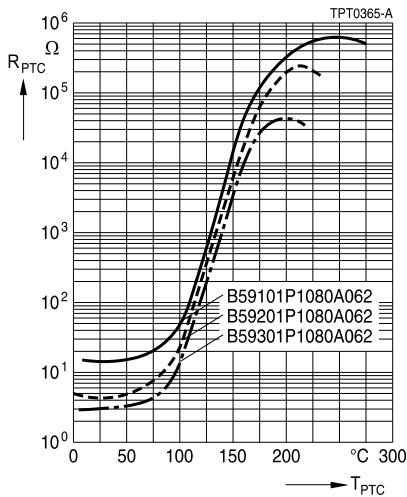
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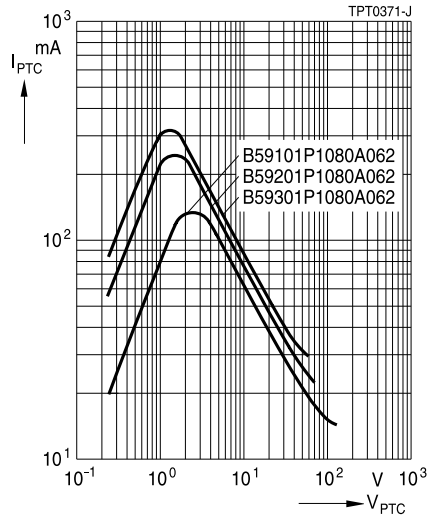
SMD

Characteristics (typical) for $T_{ref} = 80\text{ }^{\circ}\text{C}$

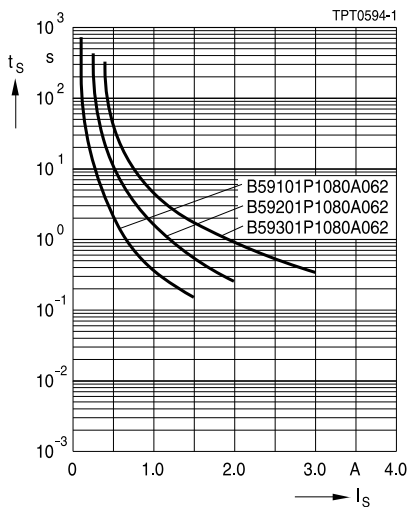
PTC resistance R_{PTC} versus
PTC temperature T_{PTC}
(measured at low signal voltage)



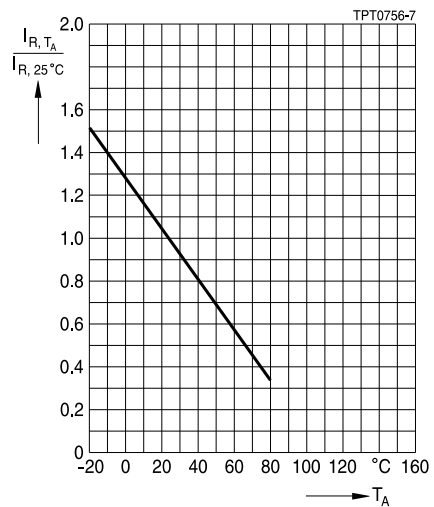
PTC current I_{PTC} versus PTC voltage V_{PTC}
(measured at $25\text{ }^{\circ}\text{C}$ in still air)



Switching time t_s versus switching current I_s
(measured at $25\text{ }^{\circ}\text{C}$ in still air)



Rated current I_R versus ambient temperature T_A
(measured in still air)



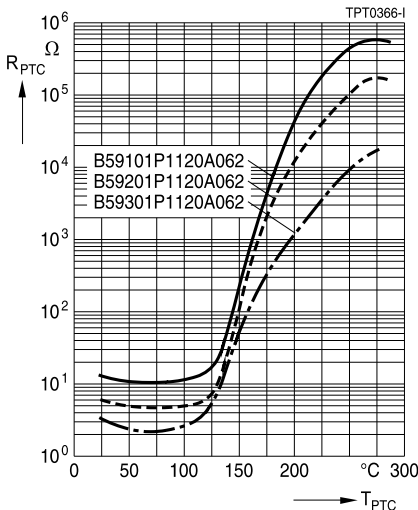
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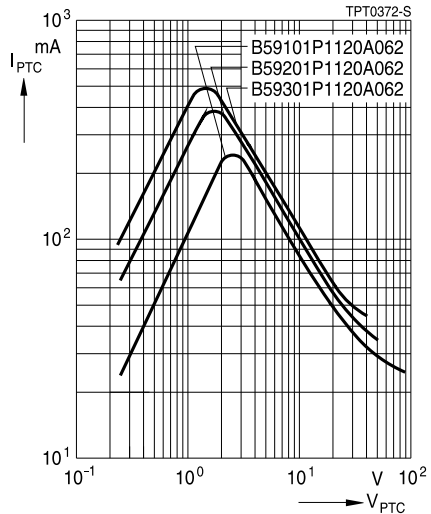
SMD

Characteristics (typical) for $T_{ref} = 120\text{ }^{\circ}\text{C}$

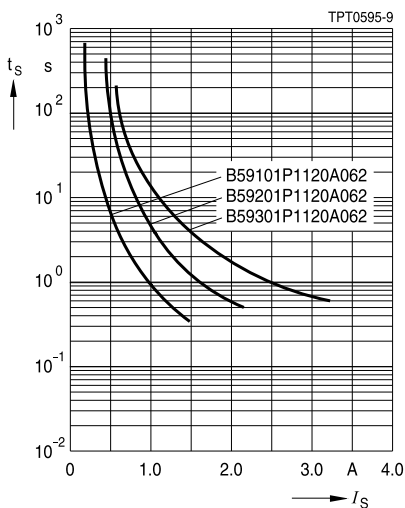
PTC resistance R_{PTC} versus
PTC temperature T_{PTC}
(measured at low signal voltage)



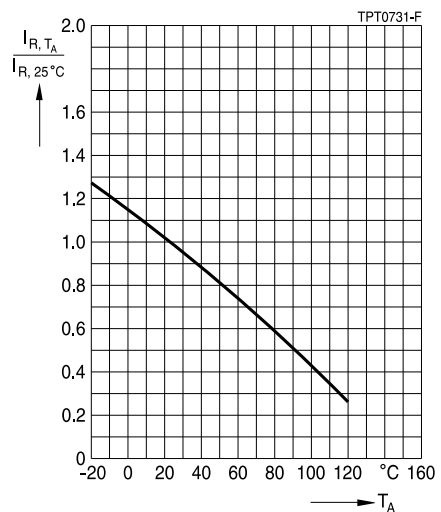
PTC current I_{PTC} versus PTC voltage V_{PTC}
(measured at $25\text{ }^{\circ}\text{C}$ in still air)



Switching time t_s versus switching current I_S
(measured at $25\text{ }^{\circ}\text{C}$ in still air)



Rated current I_R versus ambient temperature T_A
(measured in still air)



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Cautions and warnings

General

- EPCOS thermistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of thermistor through reliability testing during the design-in phase. The thermistors should be evaluated taking into consideration worst-case conditions.

Storage

- Store thermistors only in original packaging. Do not open the package prior to processing.
- Storage conditions in original packaging: storage temperature $-25\text{ }^{\circ}\text{C} \dots +45\text{ }^{\circ}\text{C}$, relative humidity $\leq 75\%$ annual mean, maximum 95%, dew precipitation is inadmissible.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environment with effect on function on long-term operation (examples given under operation precautions).
- Use thermistor within the following period after delivery:
 - Through-hole devices (housed and leaded PTCs): 24 months
 - Motor protection sensors, glass-encapsulated sensors and probe assemblies: 24 months
 - Telecom pair and quattro protectors (TPP, TQP): 24 months
 - Leadless PTC thermistors for pressure contacting: 12 months
 - Leadless PTC thermistors for soldering: 6 months
 - SMDs in EIA sizes 3225 and 4032, and for PTCs with metal tags: 24 months
 - SMDs in EIA sizes 1210 and smaller: 12 months

Handling

- PTCs must not be dropped. Chip-offs must not be caused during handling of PTCs.
- The ceramic and metallization of the components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

Soldering (where applicable)

- Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.
- Standard PTC heaters are not suitable for soldering.

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Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housing used for assembly with thermistor have to be clean before mounting. Especially grease or oil must be removed.
- When PTC thermistors are encapsulated with sealing material, the precautions given in chapter "Mounting instructions", "Sealing and potting" must be observed.
- When the thermistor is mounted, there must not be any foreign body between the electrode of the thermistor and the clamping contact.
- The minimum force and pressure of the clamping contacts pressing against the PTC must be 10 N and 50 kPa, respectively. In case the assembly is exposed to mechanical shock and/ or vibration this force should be higher in order to avoid movement of the PTC during operation.
- During operation, the thermistor's surface temperature can be very high. Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling at the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of thermistor surface during processing.

Operation

- Use thermistors only within the specified temperature operating range.
- Use thermistors only within the specified voltage and current ranges.
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by abnormal function (e.g. use VDR for limitation of overvoltage condition).

This listing does not claim to be complete, but merely reflects the experience of EPCOS AG.

Display of ordering codes for EPCOS products

The ordering code for one and the same EPCOS product can be represented differently in data sheets, data books, other publications, on the EPCOS website, or in order-related documents such as shipping notes, order confirmations and product labels. **The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products.** Detailed information can be found on the Internet under www.epcos.com/orderingcodes

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
Symbols and terms

Symbol	Term
A	Area
C	Capacitance
C_{th}	Heat capacity
f	Frequency
I	Current
I_{max}	Maximum current
I_R	Rated current
I_{res}	Residual current
I_{PTC}	PTC current
I_r	Residual current
$I_{r,oil}$	Residual current in oil (for level sensors)
$I_{r,air}$	Residual current in air (for level sensors)
I_{RMS}	Root-mean-square value of current
I_S	Switching current
I_{Smax}	Maximum switching current
LCT	Lower category temperature
N	Number (integer)
N_c	Operating cycles at V_{max} , charging of capacitor
N_f	Switching cycles at V_{max} , failure mode
P	Power
P_{25}	Maximum power at 25 °C
P_{el}	Electrical power
P_{diss}	Dissipation power
R_G	Generator internal resistance
R_{min}	Minimum resistance
R_R	Rated resistance @ rated temperature T_R
ΔR_R	Tolerance of R_R
R_P	Parallel resistance
R_{PTC}	PTC resistance
R_{ref}	Reference resistance
R_S	Series resistance
R_{25}	Resistance at 25 °C
$R_{25,match}$	Resistance matching per reel/ packing unit at 25 °C
ΔR_{25}	Tolerance of R_{25}

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T	Temperature
t	Time
T _A	Ambient temperature
t _a	Thermal threshold time
T _C	Ferroelectric Curie temperature
t _E	Settling time (for level sensors)
T _R	Rated temperature @ 25 °C or otherwise specified in the data sheet
T _{sense}	Sensing temperature
T _{op}	Operating temperature
T _{PTC}	PTC temperature
t _R	Response time
T _{ref}	Reference temperature
T _{Rmin}	Temperature at minimum resistance
t _S	Switching time
T _{surf}	Surface temperature
UCT	Upper category temperature
V or V _{el}	Voltage (with subscript only for distinction from volume)
V _{c(max)}	Maximum DC charge voltage of the surge generator
V _{F,max}	Maximum voltage applied at fault conditions in protection mode
V _{RMS}	Root-mean-square value of voltage
V _{BD}	Breakdown voltage
V _{ins}	Insulation test voltage
V _{link,max}	Maximum link voltage
V _{max}	Maximum operating voltage
V _{max,dyn}	Maximum dynamic (short-time) operating voltage
V _{meas}	Measuring voltage
V _{meas,max}	Maximum measuring voltage
V _R	Rated voltage
V _{PTC}	Voltage drop across a PTC thermistor
α	Temperature coefficient
Δ	Tolerance, change
δ _{th}	Dissipation factor
τ _{th}	Thermal cooling time constant
λ	Failure rate
	Lead spacing (in mm)

Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or lifesaving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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