

## Product Summary

<b>BV<sub>DSS</sub></b>	<b>R<sub>DS(ON)</sub> Max</b>	<b>I<sub>D</sub> T<sub>C</sub> = +25°C</b>
-450V	4.9Ω @ V <sub>GS</sub> = -10V	-4.7A

## Description

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- Motor Control
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

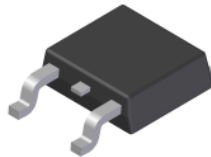
## Features

- Low Input Capacitance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input/Output Leakage
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

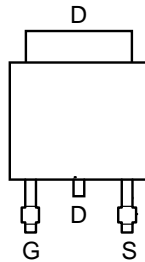
## Mechanical Data

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 <sup>(3)</sup>
- Weight: 0.33 grams (Approximate)

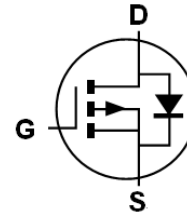
TO252 (DPAK)



Top View



Top View



Internal Schematic

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMP45H4D9HK3-13	TO252 (DPAK)	2,500/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



- Ⓜ = Manufacturer's Marking  
 45H4D9 = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Last Two Digits of Year (ex: 17 = 2017)  
 WW or WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	-450	V
Gate-Source Voltage			V <sub>GSS</sub>	±30	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = -10V	Steady State	T <sub>C</sub> = +25°C	I <sub>D</sub>	-4.7	A
		T <sub>C</sub> = +100°C		-3.0	A
Maximum Body Diode Forward Current (Note 5)			I <sub>S</sub>	-1.5	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	-4.5	A
Avalanche Current, L = 60mH (Note 7)			I <sub>AS</sub>	-2.5	A
Avalanche Energy, L = 60mH (Note 7)			E <sub>AS</sub>	187	mJ

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>C</sub> = +25°C	P <sub>D</sub>	104	W
	T <sub>C</sub> = +100°C		41	
Thermal Resistance, Junction to Ambient (Note 6)		R <sub>θJA</sub>	41	°C/W
Thermal Resistance, Junction to Case (Note 5)		R <sub>θJC</sub>	1.2	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-450	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1	µA	V <sub>DS</sub> = -450V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-3.0	-4.0	-5.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	3.1	4.9	Ω	V <sub>GS</sub> = -10V, I <sub>D</sub> = -1.05A
Diode Forward Voltage	V <sub>SD</sub>	—	—	-1.4	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -2.1A
Forward Transconductance	g <sub>fs</sub>	—	1.4	—	S	V <sub>DS</sub> = -50.0V, I <sub>D</sub> = -1.05A
<b>DYNAMIC CHARACTERISTICS (Note 7)</b>						
Input Capacitance	C <sub>iSS</sub>	—	564	—	pF	V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oSS</sub>	—	70	—		
Reverse Transfer Capacitance	C <sub>rSS</sub>	—	3.3	—		
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	13.7	—	nC	V <sub>DS</sub> = -360V, I <sub>D</sub> = -2.7A, V <sub>GS</sub> = -10V
Gate-Source Charge	Q <sub>gs</sub>	—	3.4	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	6.0	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	21	—	ns	V <sub>DD</sub> = -225V, R <sub>G</sub> = 3.0Ω, I <sub>D</sub> = -2.7A
Turn-On Rise Time	t <sub>r</sub>	—	54	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	34	—		
Turn-Off Fall Time	t <sub>f</sub>	—	34	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	168	—	ns	V <sub>GS</sub> = 0V, V <sub>DD</sub> = -200V, I <sub>S</sub> = -2.7A,
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	1.3	—	µC	dI/dt = 100A/µs

- Notes:
- Device mounted on infinite heatsink.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
  - Guaranteed by design. Not subject to production testing.
  - Short duration pulse test used to minimize self-heating effect.

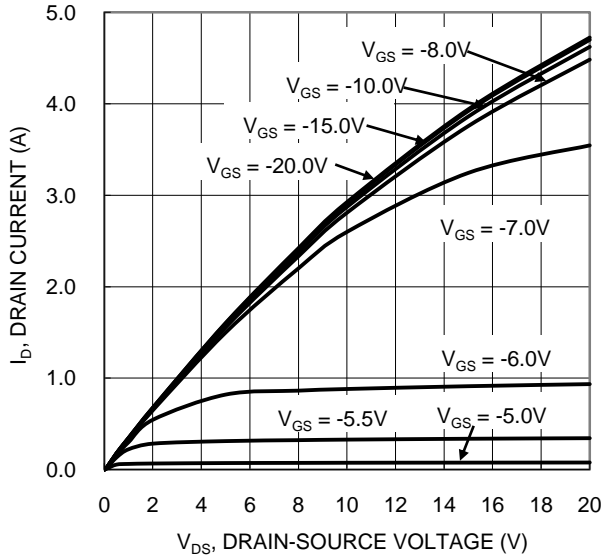


Figure 1. Typical Output Characteristic

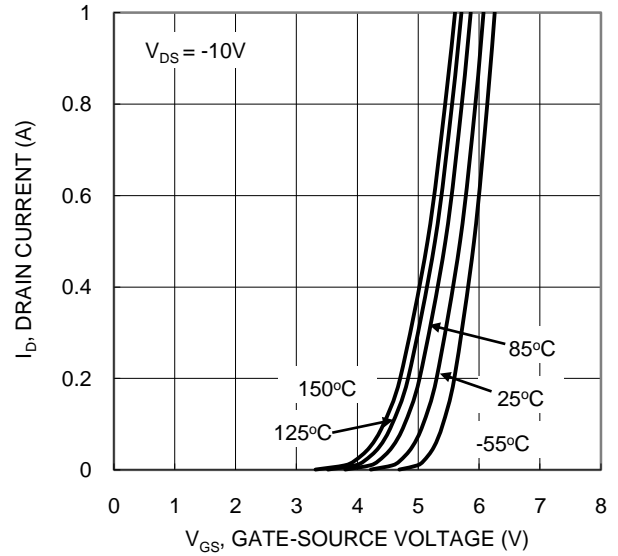


Figure 2. Typical Transfer Characteristic

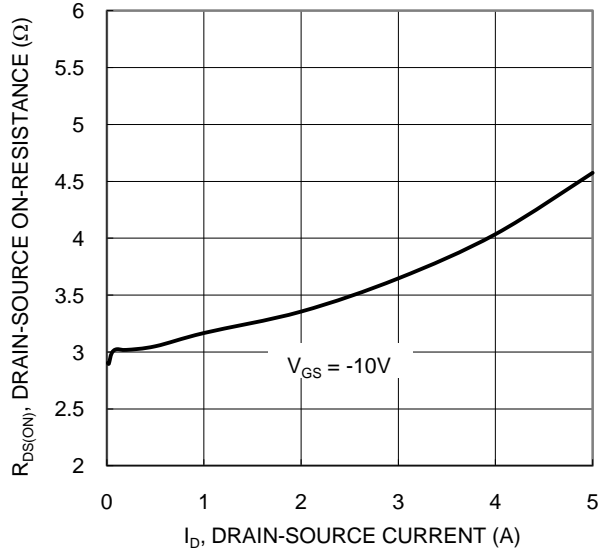


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

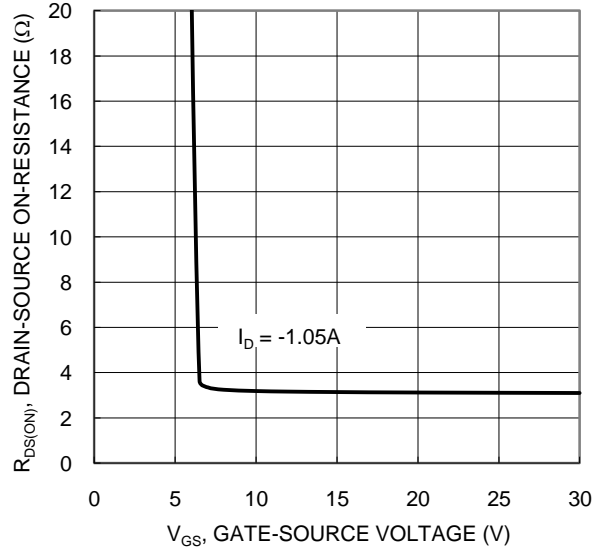


Figure 4. Typical Transfer Characteristic

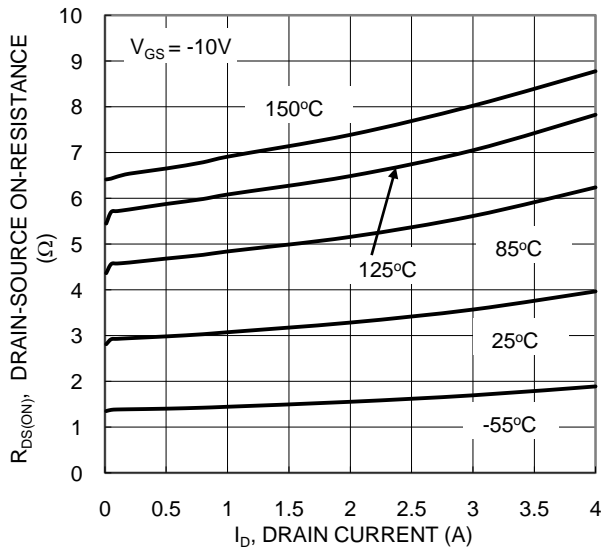


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

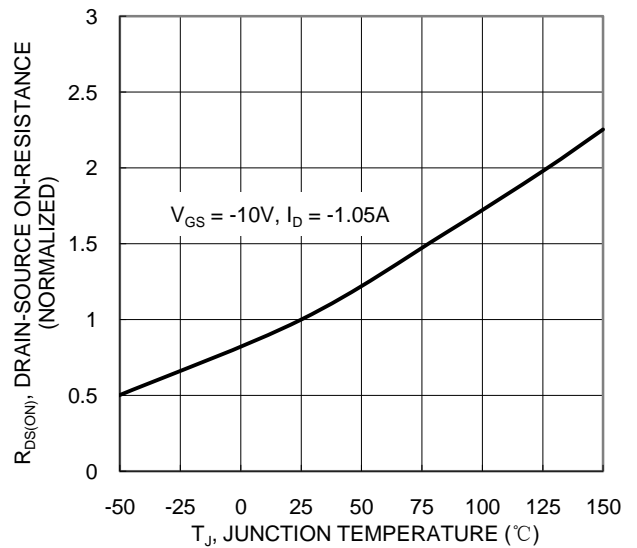


Figure 6. On-Resistance Variation with Temperature

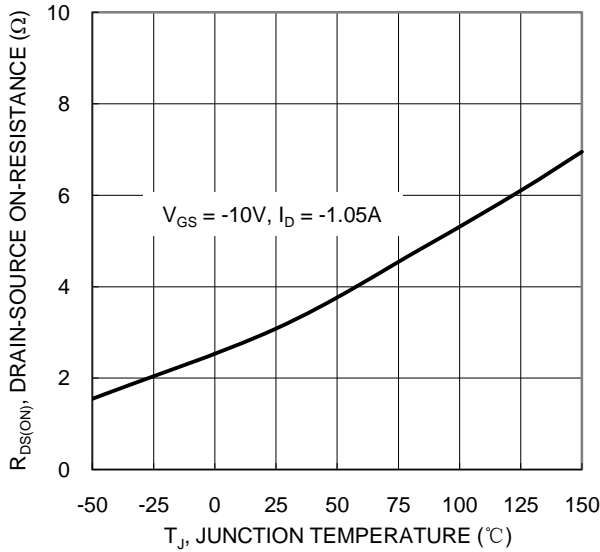


Figure 7. On-Resistance Variation with Temperature

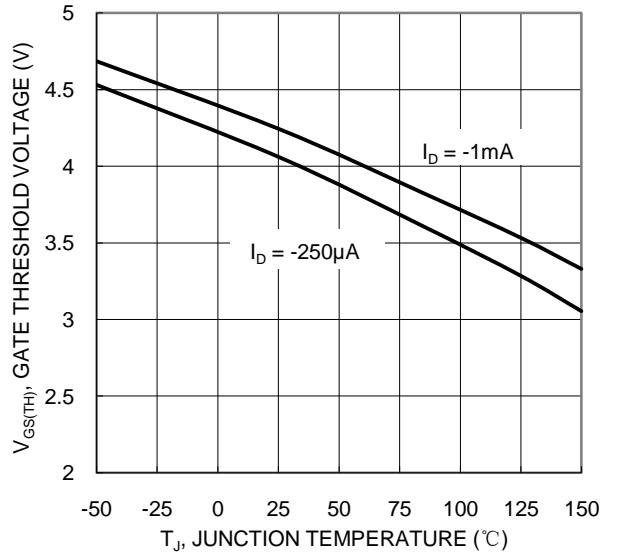


Figure 8. Gate Threshold Variation vs. Junction Temperature

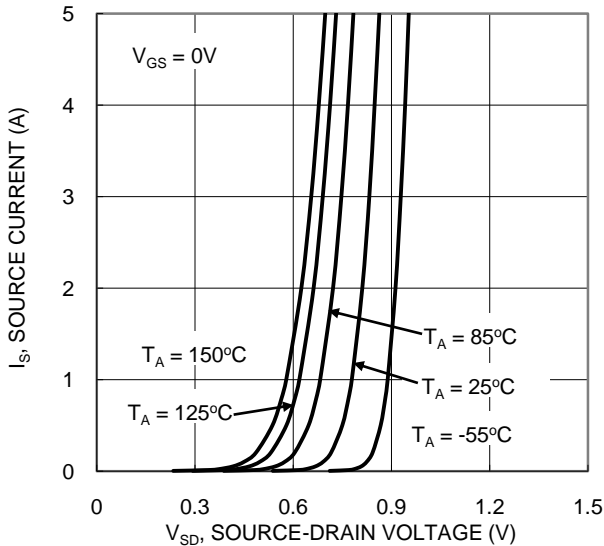


Figure 9. Diode Forward Voltage vs. Current

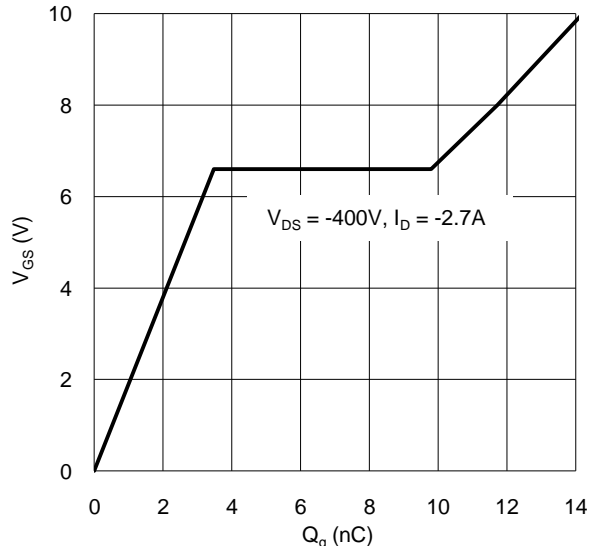


Figure 10. Gate Charge

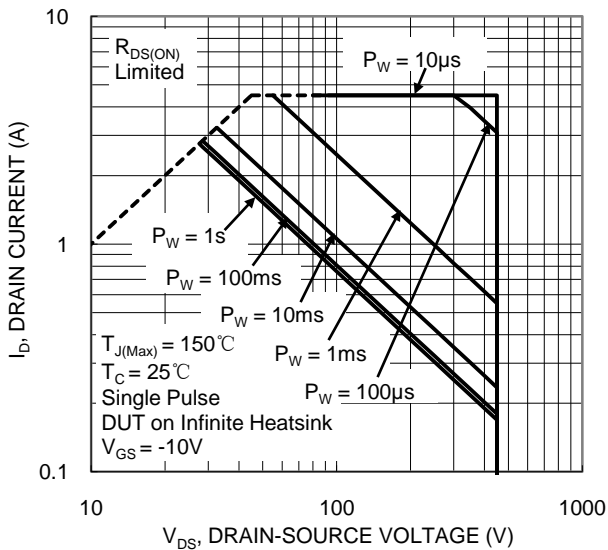


Figure 11. SOA, Safe Operation Area

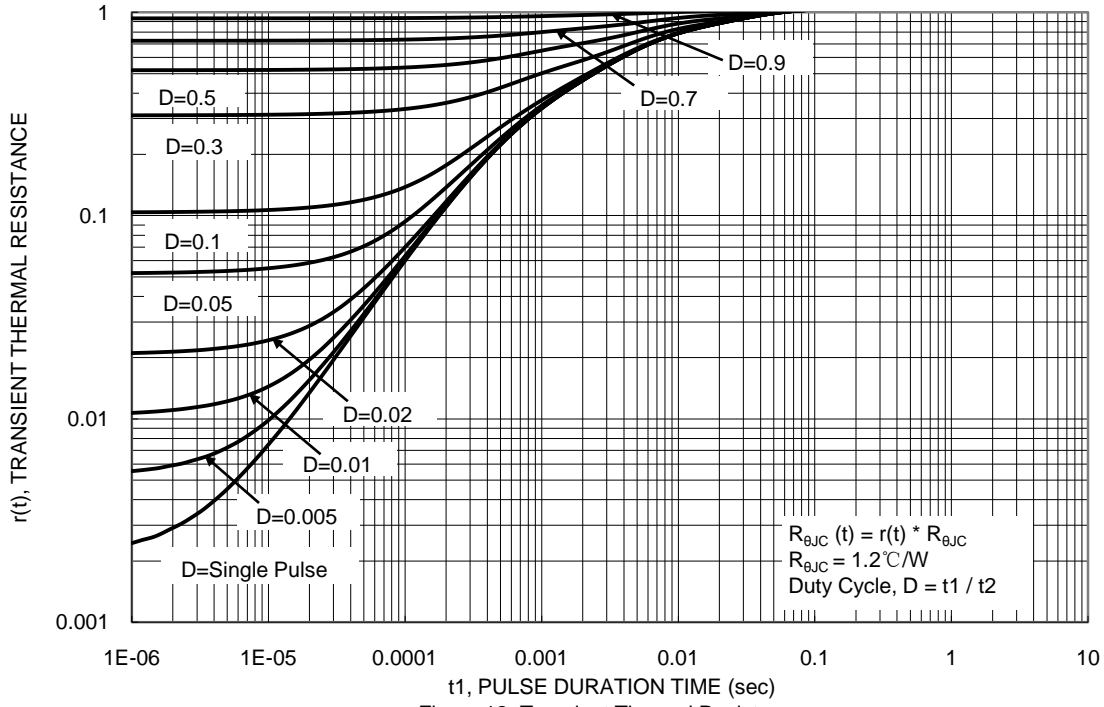
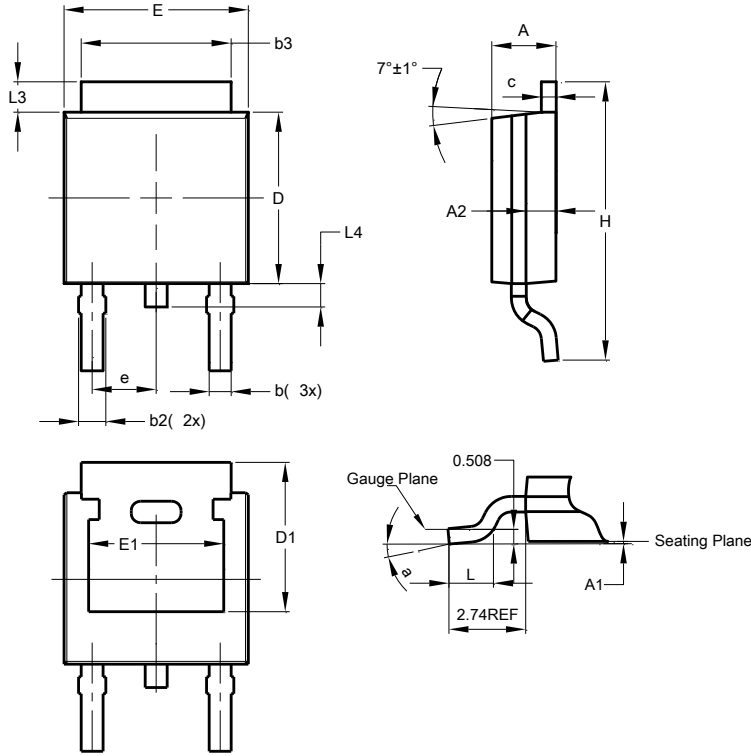


Figure 12. Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**TO252 (DPAK)**

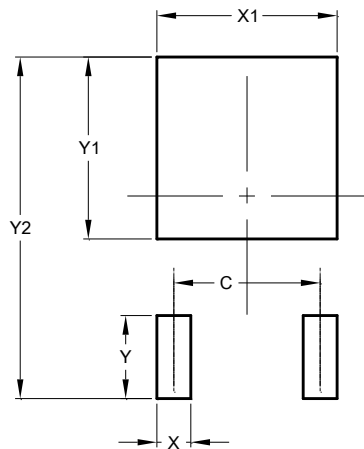


TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
<b>All Dimensions in mm</b>			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**TO252 (DPAK)**



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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